Summer Projects

VMEC Scholar Program

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2021 VMEC SUMMER SCHOLAR
BAE Systems, Inc. (Manassas, Virginia)

Company Overview:

BAE Systems plc., a global defense company, is engaged in the development, delivery, and support of advanced defense, security, and aerospace systems. BAE Systems employs 88,200 people with operations in six home countries - Australia, Saudi Arabia, South Africa, Sweden, UK, US, and customers in over 100 countries.

BAE Systems, Inc., with greater than 40,000 employees, is the US subsidiary of BAE Systems plc. The US focus is:

- Support and service solutions for current and future defense, intelligence, and civilian systems.
- Design, develop and manufacture a wide range of electronic systems and subsystems for both military and commercial applications;
- Design, develop, produce, and provide service support of armored combat vehicles, artillery systems and intelligent munitions.

Site Overview:

BAE Systems in Manassas, VA is the headquarters of the Space Systems division. We offer advanced systems and components for many types of space missions, including command and control; intelligence, surveillance, and reconnaissance; environmental and space science; communications; and navigation. These missions include notable programs like the computers for the Mars rovers, and computers for command and control of GPS satellites. We also build and test high reliability airborne RF microelectronics, for use in several airborne platforms. The Manassas site is a complete design, packaging, and test facility for development and production of CMOS devices, and production of RF microelectronic units.

Intern Responsibilities:

BAE Systems Manassas is looking for summer interns to work in various phases of integrated circuit modeling, design, and test. Job responsibilities may include simulation, data collection and analysis, hands on circuit test and debug, and process or flow development. Interns develop skills in the use of design and modeling software, and work to develop and test a host of devices such as advanced microprocessors, memories, FPGAs, ASICs and other high function devices.

Requirements:

We are interested in students completing their sophomore or junior year in electrical engineering with a desire to grow and learn about microelectronics. Students attending Virginia Tech, University of Virginia, Old Dominion University, Virginia Commonwealth University, The College of William and Mary, and George Mason University may apply.

BAE Systems is committed to a high performance culture and provides an environment that challenges our employees to be remarkable and obtain their full potential. We are an EEO/Affirmative Action Employer that understands the value of diversity and its impact on a high performance culture.
Micron Technology, Inc. (Manassas, VA)

Company Overview:
Micron is a world leader in innovating memory and storage solutions that accelerate the transformation of information into intelligence, inspiring the world to learn, communicate and advance faster than ever. We deliver the world’s broadest portfolio of technologies at the core of today’s most significant disruptive breakthroughs such as artificial intelligence and autonomous vehicles. Micron’s memory and storage solutions define what the world can do with data. Our more than 37,000 team members, in 18 different countries, work with countless customers to innovate every day and pursue the products that will shape how we live and work tomorrow.

Site Overview:
Micron Technology Virginia (MTV), located in Manassas, is Micron’s Automotive Center of Excellence and the primary manufacturing location for long lifecycle memory products including DRAM, NAND and NOR. MTV employs more than 1,600 team members and is one of the largest exporters in Virginia. The site is currently undergoing an expansion with a $3B investment and 1,100 new jobs. As a leading high-tech manufacturing company in Northern Virginia we realize a strong and healthy community is critical to the success of individuals, companies and society. MTV works closely with government and community organizations to improve the region’s quality of life and support initiatives that positively impact the Manassas and Prince William region and the Commonwealth of Virginia.

Intern Responsibilities:
Challenge yourself intellectually by working side-by-side, gaining real-world experience from leading industry professionals. You will directly play a pivotal role in continuing the aggressive growth of one of the world’s leading providers of advanced semiconductor solutions. During your internship you will work on projects in the areas of product characterization and yield analysis, new tool implementation and process enhancement and new product implementation.

Benefits:
Intern team members are eligible for Medical, Clinic, Business Travel Accident Insurance, Holiday, and Retirement at Micron (RAM) 401(k) Plan is a way for you to save for your retirement. You are immediately eligible to participate in this Plan as part of Micron Technology, Inc team. Micron will match your Pre-Tax or Roth Contributions up to 5% of your eligible annual earnings. In addition to the above benefits, Micron provides corporate housing for interns who reside outside of a 50-mile radius, as well as free Gym & Indoor swimming pool membership at nearby GMU Freedom center.

Requirements:
During their summer internship, students must be enrolled as sophomores, juniors or seniors at one of the following Institutions: Virginia Tech, University of Virginia, Old Dominion University, Norfolk State University, George Mason University, The College of William and Mary, Virginia Commonwealth University, or Virginia Military Institute, pursuing a B.S. in any of the following disciplines:
- Electrical Engineering
- Mechanical Engineering
- Chemical Engineering
- Microelectronic Engineering
- Materials Science
- Data Analytics
- Computer Engineering
- Computer Science
The Microelectronics research group at George Mason University is led by Prof. Qiliang Li and Prof. Dimitris Ioannou. The major focus of the group is on chemical and optical sensors and intelligent sensing systems.

The VMEC Summer Scholar will be working with the faculty and graduate students on one of the following **three projects**:

**(1)** gas sensors and systems based on semiconductors and nanomaterials;

**(2)** optoelectronic chemical sensors and smart systems;

**(3)** intelligent recognition and interaction based on vision and Lidar fusion.

The student will learn both hardware integration and software development in a friendly George Mason campus.

**Contact:** Prof. Qiliang Li, Dept. of Electrical and Computer Engineering, 4400 University Dr, Fairfax, VA 22030, e-mail: qli6@gmu.edu
Microwave near-fields for atom chips and ultra-cold atoms

The objective of this research is to develop tools for generating and probing atom chip-based near-fields at RF and microwave frequencies. Atom chips are used for trapping and cooling ultra-cold atoms (10 nK – 100 μK) to engineer quantum sensors.

Atom chips, have been used extensively for trapping atoms with static magnetic fields generated from DC currents, but the use of microwave near-fields for trapping has only begun to be explored. Importantly, microwave fields offer the possibility of producing spin-dependent potentials for coherent manipulation of quantum gases such as Bose-Einstein condensates. This capability is important for quantum information processing and matter-wave interferometry.

The VMEC candidate should have some knowledge and/or interest in RF and microwave engineering, EM simulation, lasers, and quantum physics.

Note: Additional projects in quantum sensing and quantum information may be available.

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Wright-Virginia Microelectronics Center at VCU

VMEC Projects

Virginia Commonwealth University (Richmond, VA) hosts 8000 ft$^2$ of state-of-the-art, class-100 cleanroom laboratories in its Wright-Virginia Microelectronics Center. We have a whole micro/nanofabrication “suit” that includes photo- and e-beam lithography, the later with a resolution as fine as 10 nm, e-beam evaporation and RF sputtering, reactive-ion etching, atomic-layer deposition, and diffusion/oxidation furnaces. The available equipment makes it possible to create and test various micro/optoelectronic, photonic, and biomedical devices.

The VCU VMEC Internship offers the unique opportunity to work independently in this high-tech facility and really “do it yourself”. Students will be able to work hands-on, from initial concept to final testing and obtain a complete micro/nanofabrication experience, including:

1) CAD layout and design of a micro-/nano-electronic, photonic, or biomedical device;
2) Photomask fabrication using our own custom mask making facilities;
3) Device fabrication in the W-VMC clean room;
4) Device testing in our characterization labs.

Training will also include standard clean room protocol and safety training. The technology for this fabrication project will vary. In the past, the projects have included MOSFETS, polymer based micromachines, surface acoustic wave (SAW) devices, solar cells, and ultra-narrow band-pass filters for short-wave infrared optical range. Our current interests encompass but not limited to micro/nano-electromechanical devices for logic applications, innovative nanophotonic devices, wearable biomedical sensor systems.

Contact: Prof. Vitaliy Avrutin, Dept. of Electrical & Computer Engineering, vavrutin@vcu.edu
University of Virginia

VMEC Projects

The University is an iconic public institution of higher education, boasting nationally ranked schools and programs, diverse and distinguished faculty, a major academic medical center and proud history as a renowned research university. The community and culture of the University are enriched by active student self-governance: https://www.virginia.edu/aboutuva

The High-Performance Low-Power (HPLP) lab: the VMEC Scholar activities will be conducted in the HPLP lab which provides all the necessary infrastructure for IC and system design and testing, space, equipment, and computing for all IC design and verification activities, including custom and semicustom design flows. This servers running CAD software packages from all major EDA vendors including Cadence, Synopsys and Mentor Graphics: https://engineering.virginia.edu/high-performance-low-power

Additionally, the UVA ECE Department has facilities that include 3500 square feet of clean room space equipped with all of the processing equipment necessary to fabricate state-of-the-art semiconductor devices, from epitaxial growth through die separation. Additional facilities are available for the electrical, optical and RF characterization of solid-state materials, devices and circuits. Significant laboratory space is also available for use in microwave and electro-optic device characterization.

Responsibilities: the VMEC scholar will be challenged intellectually by working side-by-side with PhD graduate students in the HPLP lab. During the internship the VMEC scholar will work on projects in the areas of AI/ML hardware, low-power VLSI design, IoT at the edge hardware and printable electronics.

Requirements: Currently attending one of the following Institutions: Virginia Polytechnic Institute, Old Dominion University, George Mason University, The College of William and Mary, Virginia Commonwealth University, Virginia Military Institute. Current Junior or Senior pursuing a BS in Electrical Engineering or Computer Engineering.

https://engineering.virginia.edu/high-performance-low-power/hplp-research-areas

VMEC mentor: Mircea R. Stan, ECE dept., Rice Hall 512, Charlottesville, VA 22904, mircea@virginia.edu https://engineering.virginia.edu/faculty/mircea-r-stan
The VMEC Summer Interns will work at the Off-Campus ODU-Applied Research Center located within the campus of Thomas Jefferson National Accelerator Facility in Newport News, Virginia 23606

**Project 1: Fabrication of nanoscaled Thermoelectric thin Films with Atomic Layer Deposition (ALD) Technology and Seebeck Measurements**

Thermoelectrics is a green renewable energy technology which can significantly contribute to power generation due to its potential in generating electricity out of waste heat. The main challenge for the development of thermoelectrics is its low conversion efficiency. One key strategy to improve conversion efficiency is reducing the thermal conductivity of thermoelectric materials. In this project Nanostructuring of Thermoelectric films is used trying to beat the alloy limit. In principle all low dimensional nano-structures, including two-dimensional quantum wells or superlattices, one-dimensional nano-wires or nano-tubes, and zero-dimensional quantum dots, confine the motion of electrons in one or more dimensions, which decouples the dependence between the Seebeck coefficient $S$, the electrical conductivity $\sigma$ and thermal conductivity $\kappa$, and make it possible to enhance the power factor product of $S^2\sigma$.

**Project 2: Synthesis of novel 12CaO7Al2O3 Electride Material for Electron Emission**

In this project the 12CaO·7Al2O3 electride, a sub-nanoporous compound having a work function of 2.4 eV, will be synthesized as a candidate cathode material in fluorescent lamps among other applications. The 12CaO·7Al2O3 (C12A7) electride is a new electronic oxide compound with a work function as low as 2.4 eV, which is close to those of alkaline metals. Its crystal structure can be regarded as a stack of cage-like subunits that share their faces, and this arrangement differs from that of zeolite-based compounds. For this project we shall try to synthesize this electride from ALD CaII amidinate precursor: Bis(N,N-di-i-propylformamidinato) Calcium(II) - Ca(pr-fmd)$_2$ and react it with either oxygen or DI H$_2$O. This project requires successful synthesis of a multicomponent thin film nanolaminate composed of alternating layers of Al$_2$O$_3$ and CaO which need to be alloyed by subsequent high temperature annealing to attain the required Cage structure of the C12A7 electride.

**Project 3: Novel Design of multiple Nested Coaxial ZnO Nanotube Solid-State MOS Gas Sensors Synthesized in Porous Templates**

In these projects ZnO nanotubes are to be grown and synthesized by hydrothermal solution methods on porous templates. ZnO nanotube nanostructures will be employed for gas sensing of ethanol vapor concentrations, which is aided by their high electrochemical stability, nontoxicity, and, especially, high surface-to-volume ratio. The sensing performance of ZnO nanotube gas sensors to ethanol vapor will be investigated with a gas sensor testing system equipped with a sealed reaction chamber and control system with stable temperature control and accurate concentration control.

**Mentor:** Dr. Helmut Baumgart, VMEC Professor, Department of Electrical and Computer Engineering, Old Dominion University, Norfolk, VA and ODU-Applied Research Center at Jefferson Labs, Newport News, Virginia 23606, e-mail: hbaumgar@odu.edu
VT MEMS Lab has been pursuing research in MEMS, nanotechnology, and Microfluidics (MnM) to develop highly innovative miniaturized analyzers for chemical and biomedical applications. Two major research thrusts in the lab are Micro Analytical Chemistry (MAC) and BioMEMS/NEMS (Bio).

In the MAC thrust, graduate and undergraduate students work to design and fabricate micro instruments that can be used for real-time analysis of gaseous samples. Examples of applications include environmental monitoring, assessment of personal exposure to hazardous chemicals, breath analysis for possible disease diagnosis.

In the Bio thrust, graduate and undergraduate students work to design and fabricate microfluidic chips by which they can assess the physical properties of living cells. These properties which include electrical and mechanical properties at single cell level can potentially be used to distinguish cancerous cells and to assess the efficacy of therapeutics.

Undergraduate students involved in various aspects of VT MEMS lab will be trained on layout design, chip manufacturing, and testing the performance of the chips and the instruments using the chips. Interested students can contact Professor Masoud Agah, agah@vt.edu and can take a closer look at the projects by visiting www.agah-lab.org.
1. Fabrication of silicon nanowire arrays for reducing surface reflection

Silicon nanowire (SiNW) arrays are useful to reduce surface reflection of silicon substrates significantly below its 35% reflectivity. The reduction in reflectivity should lead to enhanced performance of silicon detectors, solar cells and other optoelectronic devices. This project will investigate the antireflection properties of SiNW and any resultant enhancement in quantum efficiency of such devices.

The student will fabricate SiNW using Ag assisted catalytic etching method that is simple and does not require expensive equipment to fabricate large area samples. A high degree of control on the diameter, length, and density of SiNW can be achieved using the AgNO3/HF solution that provides the Ag nanoparticles for catalytic etching. The VMEC summer project will focus on the following topics:

1. Metal assisted etching of Si surface to produce SiNW.
2. Surface reflectivity of the nanostructured silicon surface.
3. Effect of low reflectivity of nanostructured silicon surface of a Si p-n junction.

NSU has excellent cleanroom facilities to conduct the chemical fabrication. The surface structures will be studied using high resolution scanning electron microscopes and atomic force microscopes. Reflectivity will be measured using integrating sphere and spectrometer. Si p-n junctions will be fabricated using diffusion and/or ion implantation and annealing. Quantum efficiency will be measured using our solar cell research facility.

2. Design & fabrication of optofluidic chip for biological electrophysiology measurements

Excitable biological cells exhibit mechano-electric sensitivity by which their electrical behavior is modulated by mechanical stimuli or stretch. Specialized stretch-activated ion channels in cells are thought to be responsible for this mechano-electric modulation. The goal of this project is to design a microfluidic-based platform to record electrophysiological currents from biological cells in their stretched conditions. A microfluidic chamber consisting of an array of micro-wells will be designed as depicted in the schematic below. Each microwell will be equipped with a microelectrode recording assembly underneath the substrate. The cells will be trapped and guided to the microwells using computer-controlled optical tweezers. A novel optical non-contact cell stretching method using counter-propagating laser beams, carried to the microwells via optical waveguides, will be designed to produce a controlled stretch in the trapped cells. The proposed optofluidic chip will be used to systematically characterize the stretch-activated ion channels in biological cells. The proposed research has potential to provide mechanistic breakthroughs in our understanding of several chronic diseases such as heart failure and hypertension.

Contact: Dr. Sacharia Albin  
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3. Optofluidic Non-Contact Cell-Characterization Platform to Identify Breast Cancer

Breast cancer is the second leading cause of cancer deaths among women in the United States. Majority of the deaths is due to disease spreading to other parts of the body (metastasis) and impairment of vital body functions. Early detection and accurate diagnosis of metastatic breast cancer remains a challenge despite the widespread use of mammography, due to limited sensitivity and specificity. We plan to develop a microfluidic platform integrated with two novel screening technologies, optical tweezers and Surface Enhanced Raman Spectrometry (SERS), to identify cancer cells among a cell population at very early stages of the disease. By performing two independent but complementary cell characterizations, it is possible to identify the presence of breast cancer in cells even before the structural disease manifestation begins to show up. The project has three major components: a) Development of on-chip dual-laser-beam optical trap (OT)/stretching assembly, 2) Design and fabrication of novel plasmonic SERS substrates to amplify Raman signal, and 3) Experimentation using the OT/SERS profiling platform to characterize breast cancer cells in a microfluidic platform.

The student will work in a team with other graduate students and faculty involved in the project. The student will get an opportunity to use the modern cleanroom facility in the Department of Engineering, and learn experimental techniques and algorithm development in Matlab and C++.

Contact: Dr. Sacharia Albin, Professor, Department of Engineering, Norfolk State University. Email: salbin@nsu.edu, Web: http://www.nsu.edu/engineering